#3

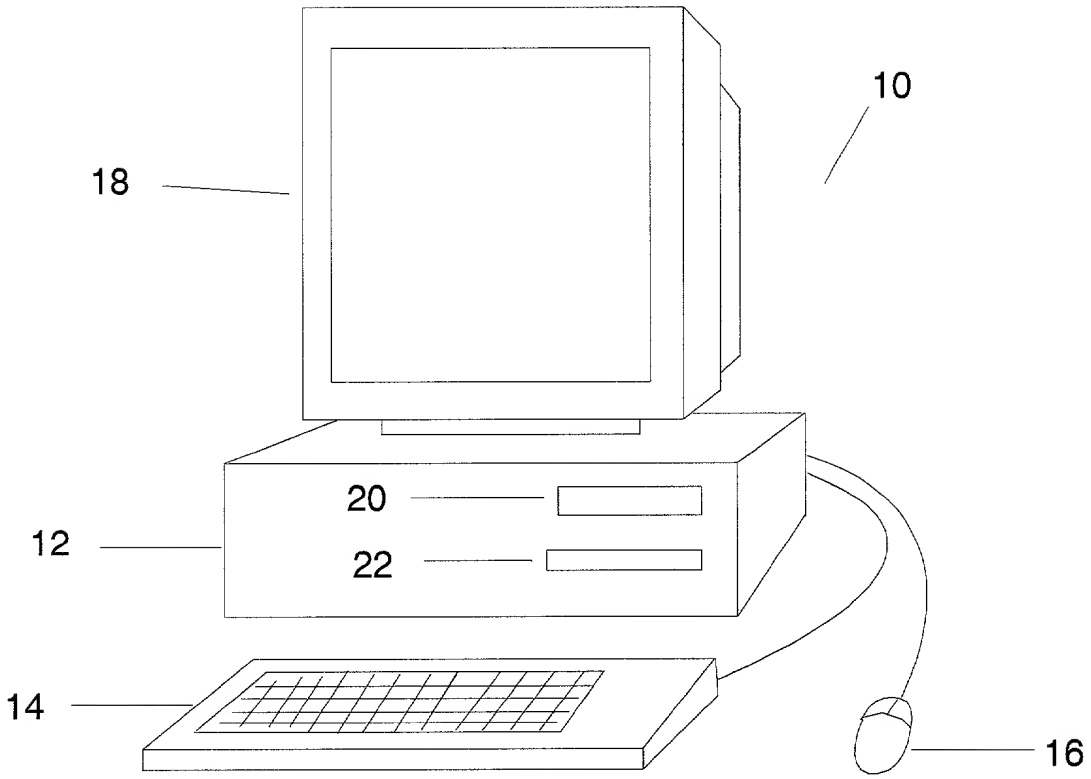

Fig. 1

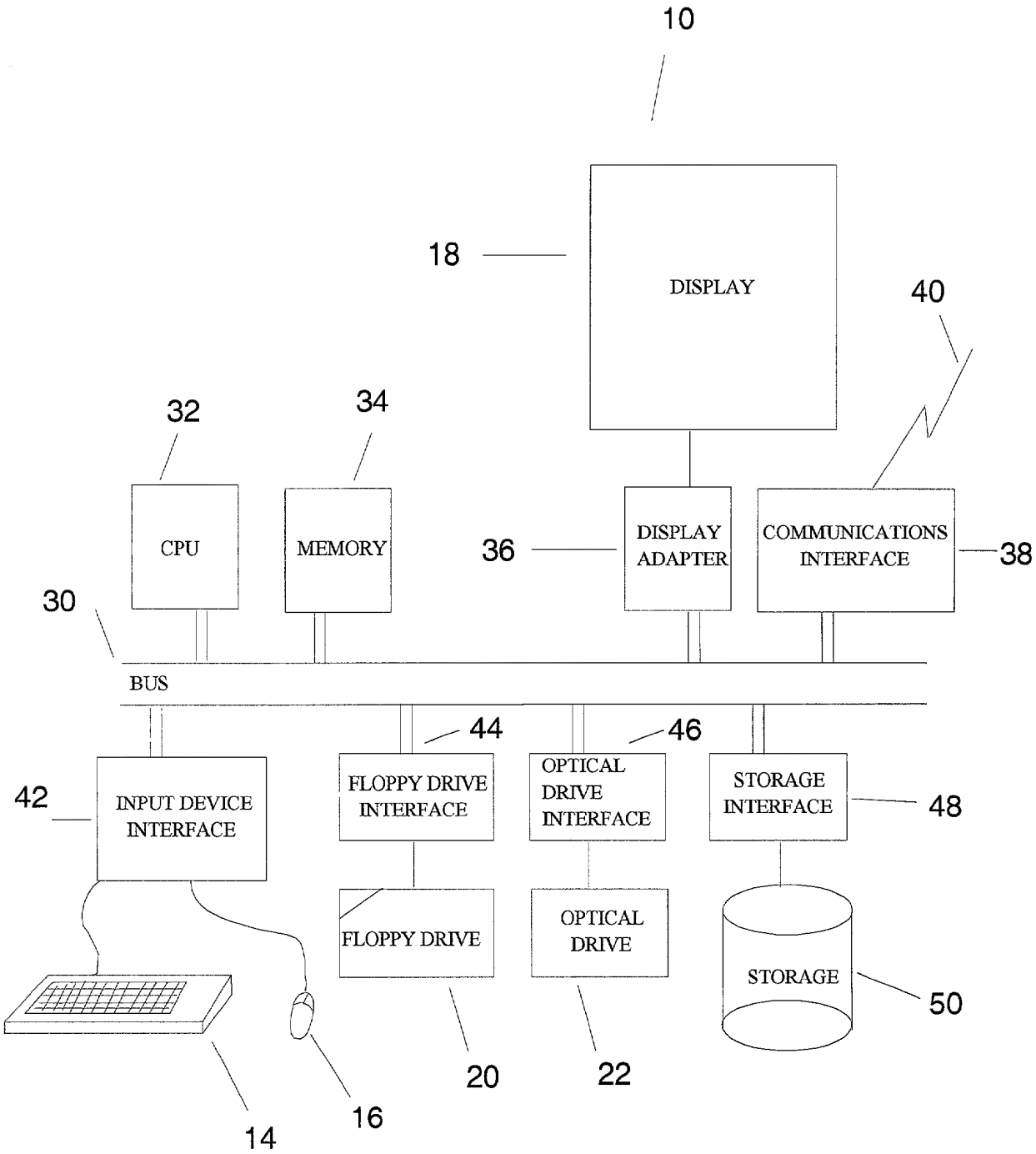
10
18
DISPLAY
40
32
34
CPU
MEMORY
36
DISPLAY ADAPTER
COMMUNICATIONS INTERFACE
38
30
BUS
44
46
42
INPUT DEVICE INTERFACE
FLOPPY DRIVE INTERFACE
OPTICAL DRIVE INTERFACE
STORAGE INTERFACE
48
FLOPPY DRIVE
OPTICAL DRIVE
STORAGE
50
20
22
14
16

Fig. 2

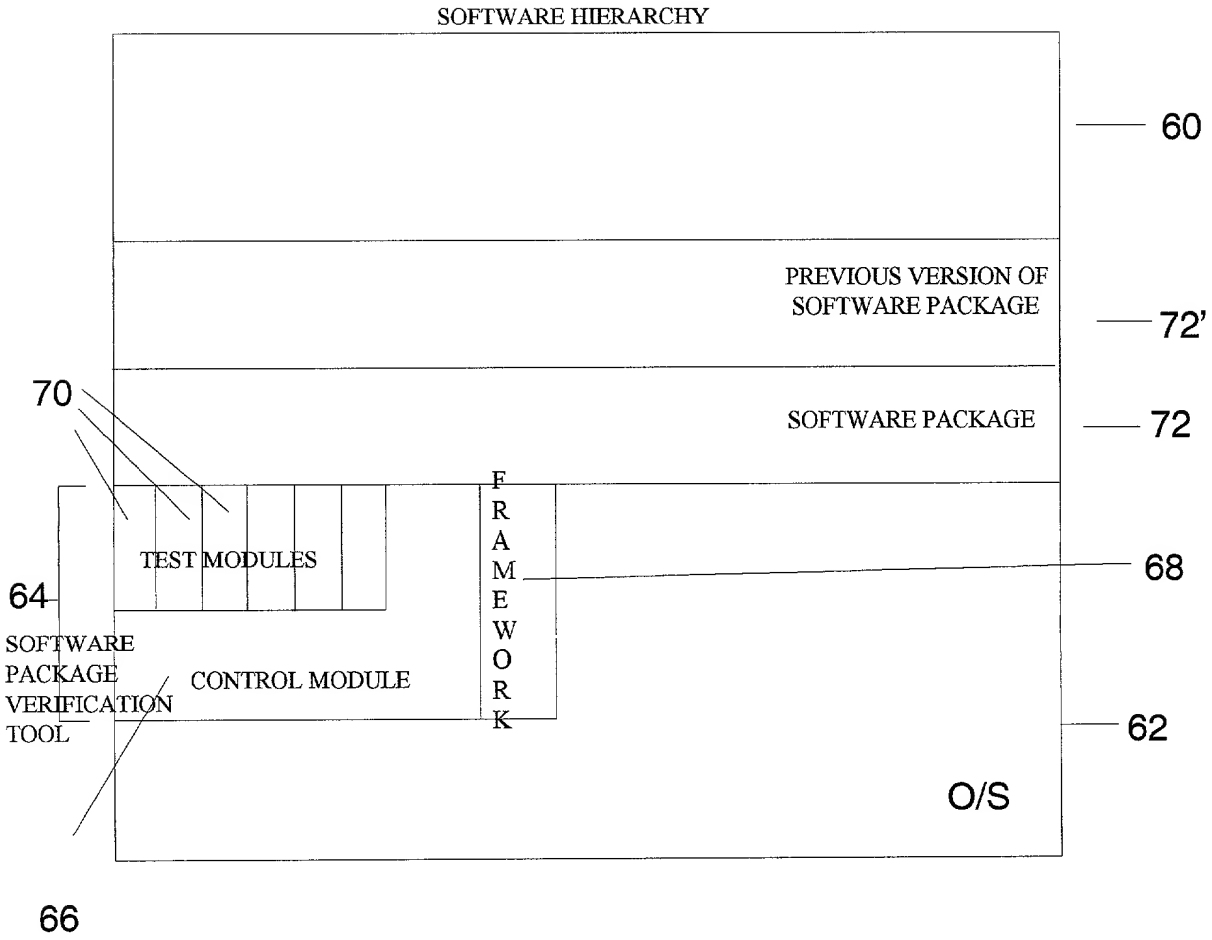
SOFTWARE HIERARCHY
60
PREVIOUS VERSION OF SOFTWARE PACKAGE
72'
70
SOFTWARE PACKAGE
72
TEST MODULES
FRAMEWORK
68
64
SOFTWARE PACKAGE VERIFICATION TOOL
CONTROL MODULE
62
O/S
66

Fig.3

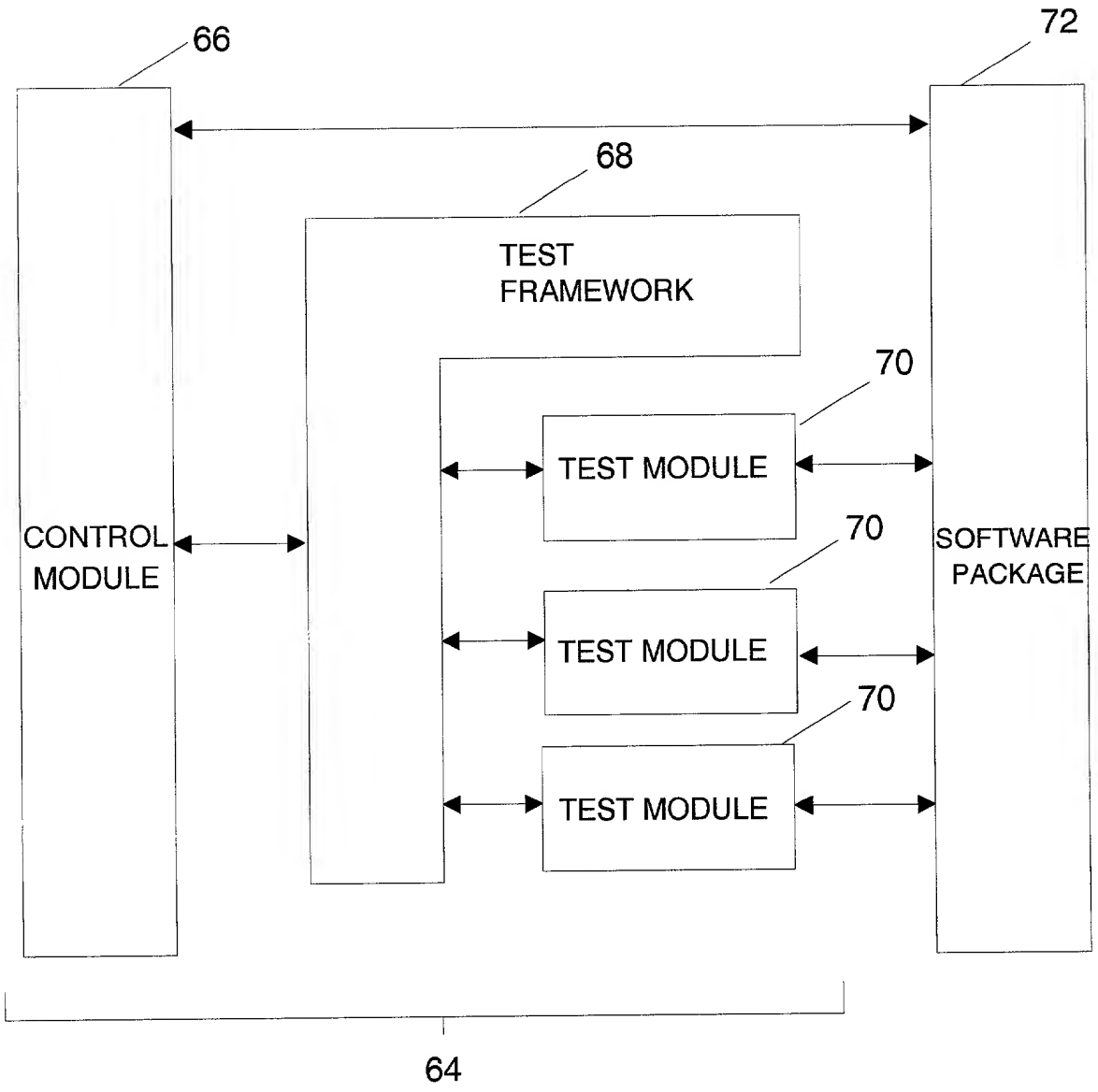

Fig. 4

SOFTWARE PACKAGE
VERIFICATION TOOL

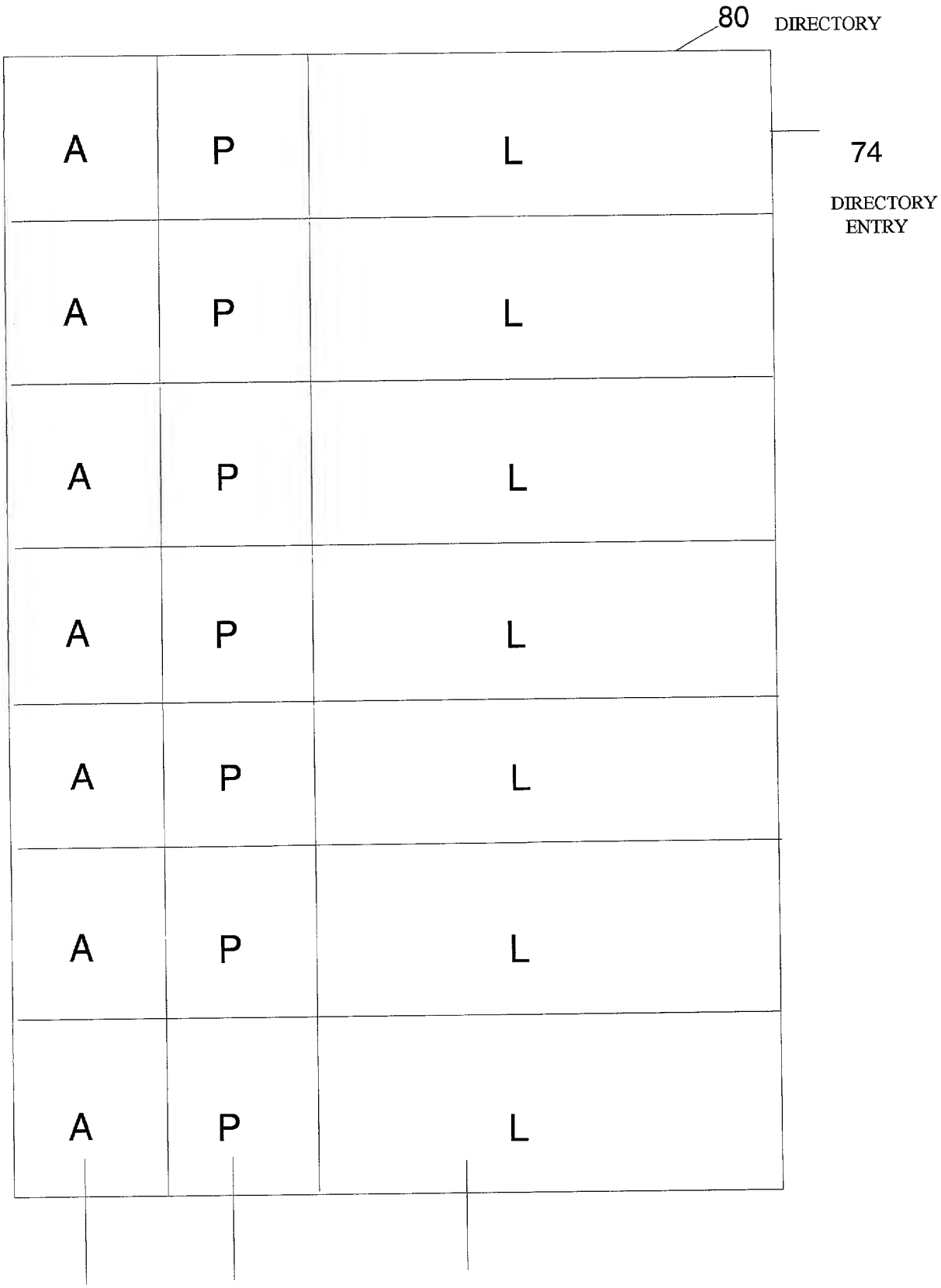
80
DIRECTORY
A
P
L
74
DIRECTORY ENTRY
75
ACTIVE INDICATOR
78
PRIORITY INDICATOR
76
LINK TO A TEST MODULE

Fig 5

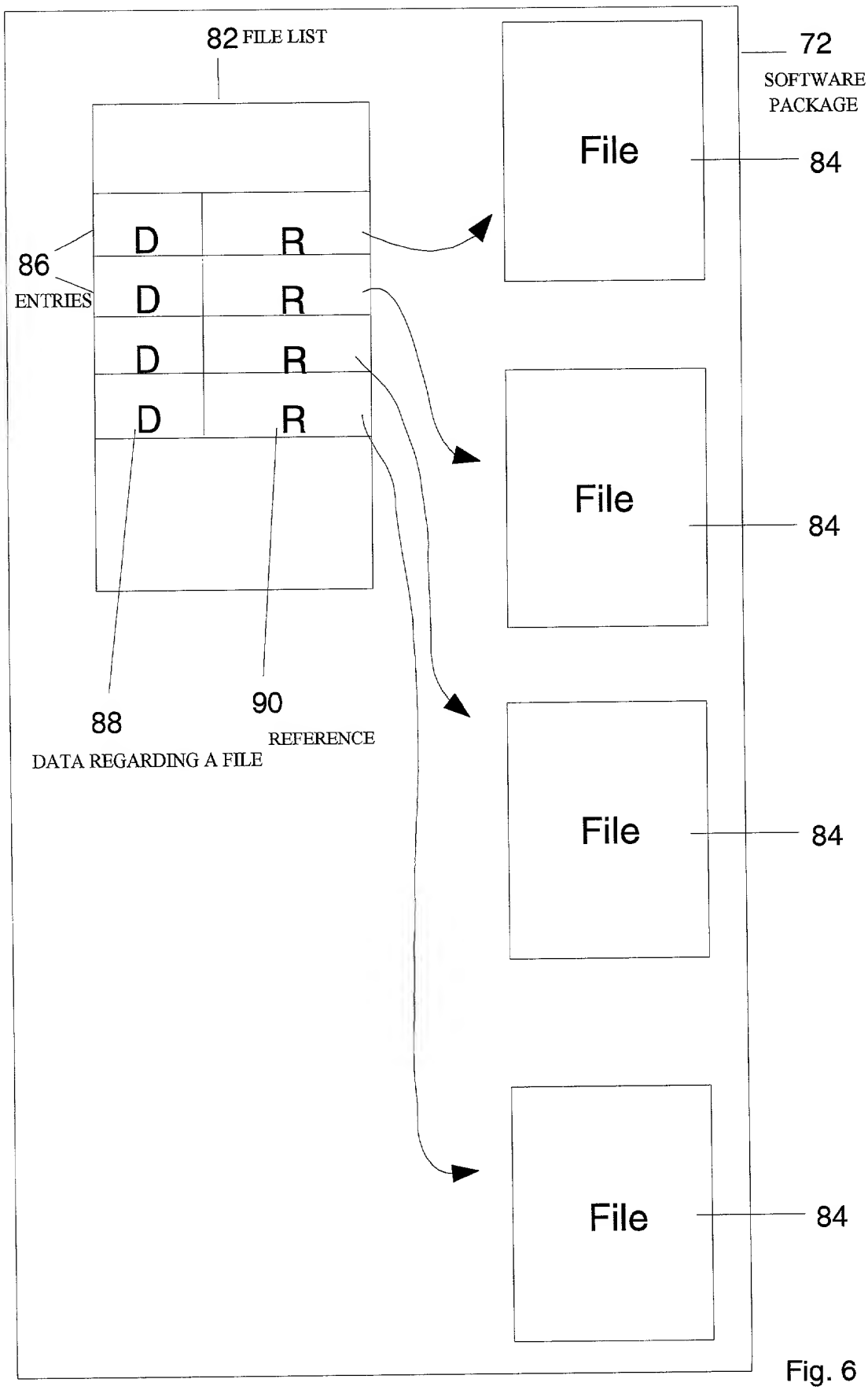
82 FILE LIST
72
SOFTWARE PACKAGE
File
84
D
R
86
ENTRIES
88
DATA REGARDING A FILE
90
REFERENCE

Fig. 6

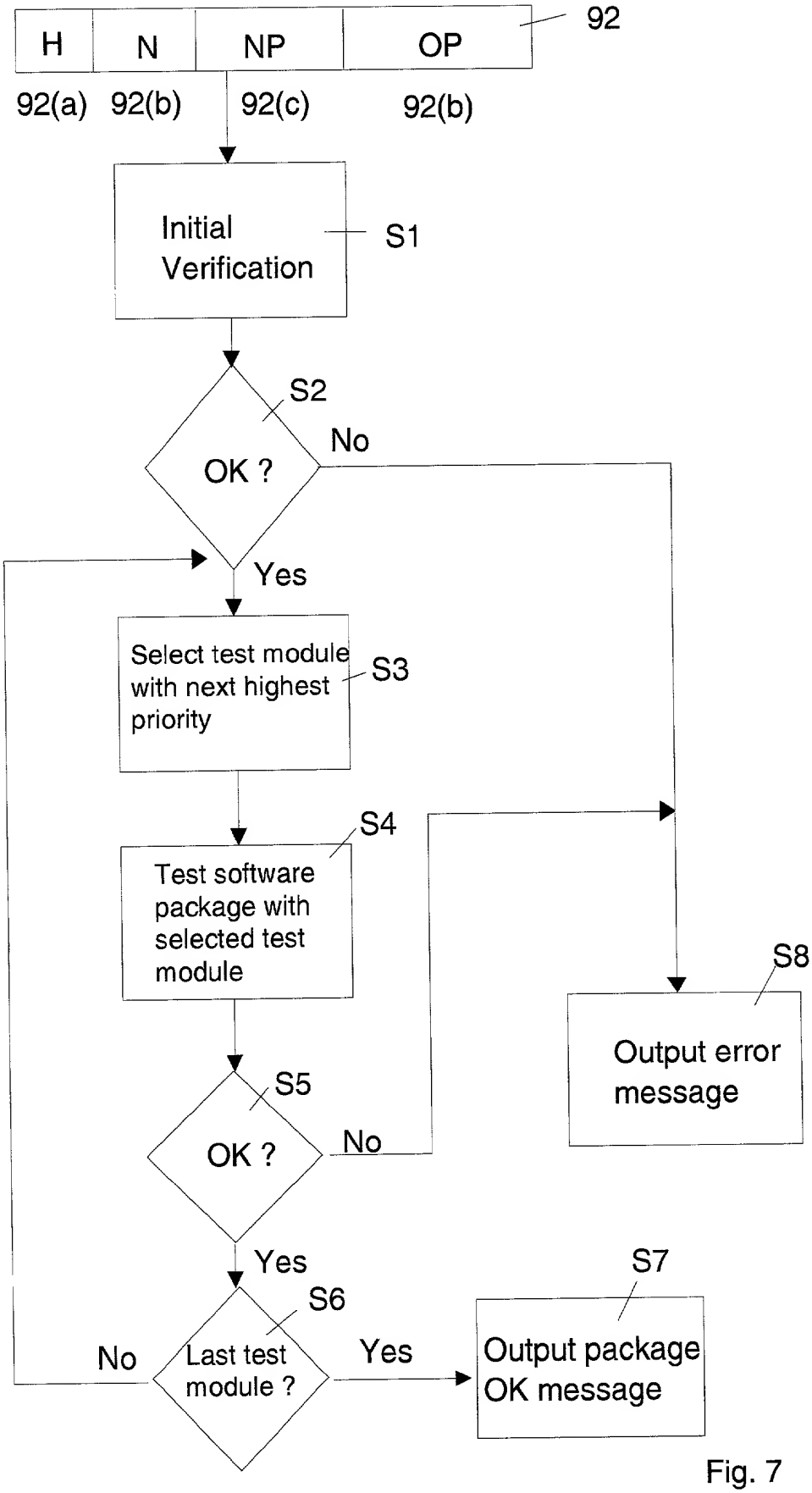
H
N
NP
OP
92
92(a)
92(b)
92(c)
92(b)
Initial Verification
S1
S2
OK ?
No
Yes
Select test module with next highest priority
S3
S4
Test software package with selected test module
S5
OK ?
No
Yes
S6
Last test module ?
No
Yes
S7
Output package OK message
S8
Output error message

Fig. 7